

## ABSTRACT OF THE DISCLOSURE

A device, such as an Application Specific Integrated Circuit (ASIC) which has access to a memory, such as non-volatile RAM (NVRAM) or Electrically Erasable Programmable Read Only Memory (E<sup>2</sup>PROM). The device may reside in a PC or on a  
5 network interface card for providing an interface between the PC and a network, such as an Ethernet-based network. Software on the PC constructs a base packet or template for an SNMP trap PDU and stores the template into the NVRAM or E<sup>2</sup>PROM associated with the device. When the device determines the need to generate and send the SNMP trap PDU, the device can, without a CPU and without a full implementation of network  
10 layer software stacks, generate the SNMP trap PDU based on the base packet stored in the NVRAM. The device need only insert the non-static data into the packet built from the base packet before sending the packet to a communication controller, such as an Ethernet controller, which subsequently sends the packet over a network, such as the Ethernet-based network.